

Sumanth Krishnamurthy

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Objective:

- To obtain an internship/full time position in the field of Electronic Packaging to develop new skills and hone existing skills.

Educational Qualification:

- **University of Texas at Arlington**, Arlington, USA (August 2014 - Present)
M.S in Mechanical Engineering (Current GPA 3.5/4)
- **Dr. Ambedkar Institute of Technology**, Bangalore, India (August 2009 to June 2013)
B.E in Mechanical Engineering

Professional Experience:

Graduate Research Assistant, University of Texas at Arlington, December 2014 – Present

- Birth-to-death Modeling Methodology for the Optimization of Custom Board-Level Reliability, Semiconductor Research Corporation (GRC Task 2512.001)
- Material characterization for different package components.
- Assist in teaching CATIA V5 to undergraduate students.

Projects:

Design of Support System for F600 (FSAE)

- Design and analysis of rear uprights, sprocket and sprocket hub for F600.
- Achieved Mumford Linkage mountings on the upright tubes and optimized the model.

Design and Analysis of a Hinge Fit of a Basic Jet Trainer (IJT)

- Design and analysis of a hinge fitting for a Basic Jet Trainer at the Wing and Empennage division of the Aircraft Research and Design Center (ARDC) of Hindustan Aeronautics Limited (HAL).
- Achieved uniform load distribution and significant weight reduction in the hinge fitting.

Technical Papers:

Sleep Monitoring System, Grit 2012, Bangalore, India

- Discusses the use of a microcontroller to monitor a person's sleep by measuring the different blood flow levels during the various phases of sleep using an infrared receiver and a transmitter.

Skill Set:

- **Tools:** CATIA V5, SolidWorks, SolidEdge, ANSYS, NISA, MATLAB
- **Programming & Database Systems:** C, JAVA, MySQL

Professional Development & Training:

- Secured the highest grade in a certified course on CATIA V5 from Electronics Corporation of India Ltd. (ECIL).
- Successfully completed professional training on JAVA and ORACLE 11g at Tech Mahindra.